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## MECHANISM TO DISABLE DYNAMICALLY A COMPUTER AUDIO INPUT/OUTPUT CONNECTOR

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### BACKGROUND OF THE INVENTION

#### Field of the Invention

This invention relates to audio connectors, and more particularly to an apparatus and method for significantly reducing spurious noise coupled onto one audio input/output ("I/O") connector and preventing such spurious noise from  
5 interfering with a signal from another audio I/O connector.

#### Description of the Related Art

Computer systems in general and personal computer systems in particular have attained widespread use for providing computer power to many segments of today's modern society. A personal computer system can usually be defined as a  
10 desktop, floor standing, or portable microcomputer that includes a system unit having a system processor and associated volatile and non-volatile memory, a display monitor, a keyboard, one or more removable storage devices, a fixed drive storage device, and an optional printer. One of the distinguishing characteristics of these systems is the use of a system board to connect these components together  
15 electronically. These personal computer systems are designed primarily to provide independent computing power to a single user (or to a relatively small group of users in the case of personal computers that serve as computer server systems) and are inexpensively priced for purchase by individuals or small businesses. A personal computer system may also include one or more of a plurality of I/O devices (*i.e.*,  
20 peripheral devices) that are coupled to the system processor and that perform

5 producing sound effects are in increasing demand as computers are used for business applications, artistic endeavors, entertainment, and education. Computers requiring audio inputs through devices such as microphones are in increasing demand as computers are used for applications requiring audio data inputs (*e.g.*, dictation programs, video conferencing, Voice over Internet Protocol (“VoIP”), and voice  
10 recognition programs).

References to audio controllers and other integrated audio solutions, audio I/O connectors, and audio devices specifically include audiovisual controllers and other integrated audiovisual solutions, audiovisual I/O connectors, and audiovisual devices.

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audio I/O connector, reducing the quality of the input or output from the device coupled to the secondary audio I/O connector. A challenge presented by this situation is the effective and inexpensive significant reduction of the spurious noise. One method of reducing this noise involves simply grounding the primary audio I/O

5 connector when a device is coupled to a secondary I/O connector. This method, depending on the physical location of the coupling of the primary audio I/O connector to ground, may still allow the coupling of some spurious noise onto the primary audio I/O connector. Another method of reducing spurious noise coupled onto a primary audio I/O connector is to open the connection to the primary audio I/O connector  
10 when a device is coupled to a secondary I/O connector. The switches required for this, however, are manufactured in multiple units to a pack, making this method expensive to implement. Another method of reducing spurious noise coupled onto a primary audio I/O connector is to shield the connectors and their associated electrical components and cabling, but adequate shielding is also expensive to implement.

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### SUMMARY OF THE INVENTION

The present invention relates to a method of dynamically disabling a primary audio I/O connector when a device is connected to a secondary audio I/O connector and locating the disablement point to significantly reduce any spurious noise coupled onto the primary audio I/O connector and its associated electrical components and  
20 cabling. In one embodiment, a transistor disables a primary audio I/O connector by pulling it to a zero voltage level when a device is coupled to an secondary audio I/O connector.

In another embodiment, a direct-current ("DC") blocking cap is included with the disabling device to prevent spurious noise from bleeding through the circuit.

25 In another embodiment, a mechanical switch detects the coupling of an audio I/O device coupled to a secondary audio I/O connector and disables a primary audio I/O connector.

5 significantly reducing the interference coupled onto the primary audio I/O connector  
and its associated electrical components and cabling.

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Figure 1 is a block diagram of a typical computer system with which the  
15 present invention may be used.

Figure 3 is a circuit diagram of an exemplary computer audio circuit with  
 20 which the present invention can be used, including an embodiment of the present  
 invention.

The use of the same reference symbols in different drawings indicates similar or identical items.

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**DESCRIPTION OF THE PREFERRED EMBODIMENT(S)**

The following sets forth a detailed description of a mode for carrying out the invention. The description is intended to be illustrative of the invention and should not be taken to be limiting.

5 Figure 1 is a block diagram of an exemplary computer system 100 that may be found in many forms, including, for example, mainframes, minicomputers, workstations, servers, personal computers, internet terminals, notebooks, and embedded systems. Personal computer ("PC") systems, such as those compatible with the x86 configuration, include desktop, floor standing, or portable versions. A

10 typical PC system 100 is a microcomputer that includes a microprocessor (or simply "processor") 110, associated main memory 150 and control logic and a number of peripheral devices that provide I/O for the system 100. Exemplary computer system 100 is powered by a power supply 114 with voltage regulator 115. The peripheral devices often include keyboards 191, mouse-type input devices 192, and other

15 traditional I/O devices (not shown) that may include display monitors, floppy and hard disk drives, CD-ROM drives, modems, and printers. The number of I/O devices being added to personal computer systems continues to grow. For example, many computer systems also include terminal devices, televisions, sound devices, voice recognition devices, electronic pen devices, and mass storage devices such as tape

20 drives, or digital video disks ("DVDs"). The peripheral devices usually communicate with the processor over one or more peripheral component interconnect ("PCI") slots 166, universal serial bus ("USB") ports 175, or integrated device electronics ("IDE") connectors 176. The PCI slots 166 may use a card/bus controller 165 to connect to one or more buses such as host bus 120, PCI bus 160, and low pin count ("LPC") bus

25 180, with the buses communicating with each other through the use of one or more hubs such as graphics controller memory hub 140 and I/O controller hub 170. Typical systems such as exemplary system 100 often include network interface cabling slots 198 to accommodate network cards that mediate between the computer and the physical media over which transmissions to and from system 100 travel. The

30 USB ports 175 and IDE connectors 176 may connect to one or more of the hubs 140,

170. The hubs may communicate with each other through the use of one or more links such as hub link 190. Many I/O devices can also be accommodated by parallel ports 193 and serial ports 194 that are coupled to an I/O controller 187 that is in turn coupled to a LPC bus 180. Typical computer systems often include a graphics card

5 130 coupled to a graphics memory controller hub 140 by a graphics bus 135 and a main memory 150 coupled to a graphics memory controller hub 140 by a system management ("SM") bus 130. Finally, a typical computer system also includes software modules known as the basic input/output system (BIOS). The BIOS code 184 is usually stored on the firmware hub 186 in memory 183.

10 Exemplary computer system 100 in Figure 1 includes a primary audio I/O connector 145 and a secondary audio I/O connector 195 to which audio I/O devices can be coupled to connect them to the audio controller 155, and transistor 146 pulls a device coupled to primary audio I/O connector 145 to a substantially zero voltage level when a device is coupled to secondary audio I/O connector 195. A first  
15 embodiment of the present invention is represented conceptually in Figure 1 as a transistor 146 coupled to connectors 145 and 195 and ground.

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20 In the present invention, a transistor may be conceptualized as having a control terminal that controls the flow of current between a first current handling terminal and a second current handling terminal. An appropriate condition on the control terminal causes a current to flow from/to the first current handling terminal and to/from the second current handling terminal. In a bipolar NPN transistor, the first current handling terminal is the collector, the control terminal is the base, and the second current handling terminal is the emitter. A sufficient current into the base causes a collector-to-emitter current to flow. In a bipolar PNP transistor, the first  
25 current handling terminal is the emitter, the control terminal is the base, and the second current handling terminal is the collector. A current exiting the base causes an emitter-to-collector current to flow.

A FET may likewise be conceptualized as having a control terminal that controls the flow of current between a first current handling terminal and a second

current handling terminal. Although FETs are frequently discussed as having a drain, a gate, and a source, in most such devices the drain is interchangeable with the source. This is because the layout and semiconductor processing of the transistor is symmetrical (which is typically *not* the case for bipolar transistors). For an N-channel

5 FET, the current handling terminal normally residing at the higher voltage is customarily called the drain. The current handling terminal normally residing at the lower voltage is customarily called the source. A sufficient voltage on the gate causes a current to therefore flow from the drain to the source. The gate-to-source voltage referred to in an N-channel FET device equations merely refers to whichever  
10 diffusion (drain or source) has the lower voltage at any given time. To reflect the symmetry of most N-channel FETs, the control terminal is the gate, the first current handling terminal may be termed the "drain/source," and the second current handling terminal may be termed the "source/drain." Such a description is equally valid for a P-channel FET, since the polarity between drain and source voltages, and the  
15 direction of current flow between drain and source, is not implied by such terminology. Alternatively, one current handling terminal may be arbitrarily deemed the "drain" and the other deemed the "source," with an implicit understanding that the two are not distinct, but interchangeable.

Referring to Figure 2, a functional block diagram of a circuit 200, an  
20 exemplary computer audio circuit with two inputs or outputs, is shown. Circuit 200 comprises disabling device 210 and circuit element 227, which, taken together, represent a second embodiment of the present invention. Circuit element 227 couples filter circuit 220 with inverting amplifier 230 and serves as a DC blocking cap. Exemplary circuit 200 further comprises a filter circuit 220, an inverting amplifier  
25 230, and an integrating amplifier 240. Exemplary circuit 200 further comprises primary audio I/O coupling 250, secondary audio I/O coupling 255, audio input reference voltage coupling 260, audio input power coupling 265, microphone bias supply coupling 266, and primary audio input disable signal coupling 213. Exemplary circuit 200 further comprises circuit element 281, which couples audio  
30 input reference voltage coupling 260 to circuit element 227 and inverting amplifier 230 at a point in circuit 200 disposed between circuit element 227 and integrating



amplifier 230. Exemplary circuit 200 further comprises audio I/O coupling 270, which couples exemplary circuit 200 to the remainder of exemplary computer system 100.

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an* Referring to Figure 3, a circuit diagram of a circuit 300, an exemplary computer audio circuit with two inputs, is shown. Circuit 300 comprises disabling device 210 and circuit element 227, which, taken together, represent a second embodiment of the present invention. Disabling device 210 comprises circuit element 212 and FET 211 where the drain is coupled to primary audio input coupling 250, the source is coupled to ground, and the gate is coupled to primary audio input disable signal coupling 213. Filter circuit 220 comprises circuit elements 221, 222, 223, 224, 225, 226 and 228. Inverting amplifier 230 comprises circuit elements 231, 232, 233, and 234, and operational amplifier 235. Integrating filter 240 comprises circuit elements 236, 241, 242, 243, 244, and 280, and operational amplifier 245.

One skilled in the art will recognize that the foregoing components and devices in Figures 1, 2, and 3 are used as examples for the sake of conceptual clarity and that various configuration modifications are common. Consequently, as used herein the specific exemplars set forth in Figures 1, 2, and 3 are intended to be representative of their more general classes. In general, use of any specific exemplar herein is also intended to be representative of its class, and the non-inclusion of such specific devices in the foregoing list should not be taken as indicating that limitation is desired.